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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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09/550,642

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EXAMINER

ODOM, CURTIS B

ART UNIT

PAPER NUMBER

2634

DATE MAILED: 06/07/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/550,642

Applicant(s)

SORRELLS ET AL.

Examiner

Curtis B. Odom

Art Unit

2634

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 12 March 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 75-119 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☐ Claim(s) 75,76,79-87,90-100,103-110 and 113-119 is/are rejected.
- 7) ☒ Claim(s) 77,78,88,89,101,102,111 and 112 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 14 April 2000 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

DETAILED ACTION

Information Disclosure Statement

1. The information disclosure statement (IDS) submitted on 12/18/02 is now in compliance with the provisions of 37 CFR 1.97. Accordingly, the information disclosure statement is being considered by the examiner.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

3. Claims 75, 79, 81-83, 86, 90, 91-94, 97, 98, 105, 106, 109, 113, and 115-117 are rejected under 35 U.S.C. 102(e) as being anticipated by Bonn et al (U. S. Patent No. 6, 160, 280).

Regarding claim 75, Bonn et al. discloses an apparatus (Fig. 1, block 10, column 2, line 8-column 4, line 30) for down-converting an electromagnetic signal, wherein an RF signal is an electromagnetic signal, comprising:

a capacitor (Fig. 1, elements 42 and 44) having a first and second port;

Art Unit: 2634

a switching device (Fig. 1, elements 20, 50, and 30) having a first, second and third port;
and

a resonant structure having a first and second port (Fig. 1, elements 38 and 42, capacitor and inductor);

wherein the first port of the capacitor (Fig. 1, elements 26 and 44) is electrically coupled to the second port of the switching device (elements 30 and 50), and the first port of the resonant structure (input to element 38) is electrically coupled to the first port of the switching device (Fig. 1, element 50); and

wherein a control signal (Fig. 1, LO) is electrically coupled to the third port of the switching device (Fig. 1, element 50), and an RF source signal (Fig. 1, RF) is electrically coupled to the first port of the resonant structure (Fig. 1, elements 38 and 42).

Regarding claim 79, which inherits the limitations of claim 75, Bonn et al. discloses the first port of the capacitor (Fig. 1, elements 26 and 44) is electrically coupled to an impedance matching network (Fig. 1, block 11, column 2, lines 8-27).

Regarding claim 81, which inherits the limitations of claim 75, Bonn et al. discloses the first port of the resonant structure (Fig. 1, elements 38 and 42) is coupled to an impedance matching network (Fig. 1, block 11, column 2, lines 8-27).

Regarding claim 82, which inherits the limitations of claim 75, Bonn et al. discloses the switching device is a transistor (column 2, line 66-column 3, line 12).

Regarding claims 83, which inherit the limitations of claim 75, Bonn et al. discloses the switching device is a FET (column 2, line 66-column 3, line 12).

Art Unit: 2634

Regarding claim 86, Bonn et al. discloses an apparatus (Fig. 1, block 10, column 2, line 8-column 4, line 30) for down-converting an electromagnetic signal, wherein an RF signal is an electromagnetic signal, comprising:

a first and second (Fig. 1, elements 26 and 44) capacitor each having a first and second port;

a switching device (Fig. 1, elements 20, 50, and 30) having a first, second and third port; and

a resonant structure having a first and second port (Fig. 1, elements 38 and 42, capacitor and inductor);

wherein the first port of the capacitor (Fig. 1, element 26) is electrically coupled to the second port of the switching device (element 50), the first port of the second capacitor (Fig. 1, element 44) is electrically coupled to the first port of the switching device, the second port of the second capacitor is electrically coupled to the second port of the switching device, and the first port of the resonant structure is electrically coupled to the first port of the switching device; and

wherein a control signal (Fig. 1, LO) is electrically coupled to the third port of the switching device, and an RF source signal (Fig. 1, RF) is electrically coupled to the first port of the resonant structure.

Regarding claims 90, and 92-94, which inherit the limitations of claim 86, the claimed device includes features corresponding to subject matter mentioned above in the rejection of claims 79, and 81-83 which is applicable hereto.

Regarding claim 97, Bonn et al. discloses an apparatus (Fig. 1, block 10, column 2, line 8-column 4, line 30) for down-converting an electromagnetic signal, wherein an RF signal is an electromagnetic signal, comprising:

a capacitor (Fig. 1, elements 26 and 44) having a first and second port;
a first and second switching device (Fig. 1, elements 30 and 50) each having a first, second and third port; and

wherein the first port of the capacitor (element 26) is electrically coupled to the second port of the first switching device (element 50), and the second port of the capacitor is electrically coupled to the second port of the second switching device (element 30), and the third port of the first switching device is electrically coupled to the third port of the second switching device (Fig. 1, elements 30 and 50); and

wherein a control signal (Fig. 1, LO) is electrically coupled to the third port of the first switching device and the third port of the second switching device, and an RF source signal (Fig. 1, RF) is electrically coupled to the first port of the first switching device (Fig. 1, element 50) and the first port of the second switching device (Fig. 1, element 30).

Regarding claim 98, which inherits the limitations of claim 97, Bonn et al. discloses a resonant structure having a first and second port (Fig. 1, elements 38 and 42),

wherein the first port of the resonant structure is electrically coupled to the first port of the first switching device (Fig. 1, element 50) and second port of the resonant structure is coupled to the first port of the second switching device (Fig. 1, element 30).

Regarding claim 105, which inherits the limitations of claim 97, Bonn et al. discloses the first and second switching devices are transistors (column 2, line 66-column 3, line 12).

Art Unit: 2634

Regarding claims 106, which inherit the limitations of claim 97, Bonn et al. discloses the switching device is a FET (column 2, line 66-column 3, line 12).

Regarding claim 109, Bonn et al. discloses an apparatus (Fig. 4 and 5, column 6, lines 3-67) for down-converting an electromagnetic signal, wherein an RF signal is an electromagnetic signal, comprising:

a first and second (Fig. 1, elements 26 and 44) capacitor each having a first and second port;
a switching device (Fig. 1, elements 20, 50, and 30) having a first, second and third port;
and

a load (Fig. 4, elements 58, 32, 46, and 48);
wherein the first port of the capacitor (Fig. 1, element 26) is electrically coupled to the second port of the switching device (element 50), the first port of the second capacitor (Fig. 1, element 44) is electrically coupled to the first port of the switching device, the load (Fig. 1, elements 46 and 48) is electrically coupled to the second port of the switching device the second port of the second capacitor is electrically coupled to the second port of the switching device,
and

wherein a control signal (Fig. 1, LO) is electrically coupled to the third port of the switching device, and an RF source signal (Fig. 1, RF) is electrically coupled to the first port of the switching device.

Regarding claim 113, which inherits the limitations of claim 109, Bonn et al. discloses the first port of the capacitor (Fig. 1, elements 26) is electrically coupled to an impedance matching network (Fig. 1, block 11, column 2, lines 8-27).

Art Unit: 2634

Regarding claim 115, which inherits the limitations of claim 109, Bonn et al. discloses the first port of the second capacitor (Fig. 1, element 44) is coupled to an impedance matching network (Fig. 1, block 11, column 2, lines 8-27).

Regarding claim 116, which inherits the limitations of claim 109, Bonn et al. discloses the switching device is a transistor (column 2, line 66-column 3, line 12).

Regarding claims 117, which inherit the limitations of claim 109, Bonn et al. discloses the switching device is a FET (column 2, line 66-column 3, line 12).

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claims 76, 80, 84, 85, 87, 91, 95, 96, 99, 100, 103, 104, 107, 108, 118, and 119 are rejected under 35 U.S.C. 103(a) as being unpatentable over Bonn et al. (U. S. Patent No. 6, 160, 280).

Regarding claims 76, 87, 100, 110, Bonn et al. does not disclose a value of capacitance for the capacitor is selected so that the capacitor discharges stored energy to a load when the switching device is open. However, Long discloses a capacitor coupled to a switching device (Fig. 1, element 20) connected in series with a load (Fig. 1, block 32). Therefore, it would have

Art Unit: 2634

been obvious to one of ordinary skill in the art at the time the invention was made that in order for the device to function properly the value of the capacitor would need to be correctly calculated for the capacitor to function properly. Thus, choosing a value for the capacitor is deemed a design choice and does not constitute patentability.

Regarding claims 80, 91, 103, 104 and 114, Bonn et al. does not disclose the first port of the capacitor is electrically coupled to an amplifier or the first and second ports of the capacitor is electrically coupled to the first and second ports of a differential amplifier. However, it would have been obvious to one of ordinary skill in the art at the time the invention was made to include the feature of an amplifier to amplify weaker signals which would allow more efficient processing.

Regarding claims 84, 85, 95, 96, 107, 108, 118, and 119, Bonn et al. does not disclose the switching device is a JFET or MOSFET. However, it would have been obvious to one of ordinary skill in the art at the time the invention was made that since these are all transistors, that each of these devices could have been used to perform the switching function. Thus, choosing a type of transistor is deemed a design choice and does not constitute patentability.

Regarding claim 99, Bonn et al. discloses a first impedance each having a first and second port (Fig. 1, block 11, column 2, lines 8-27),

wherein the first port of the first impedance is electrically coupled to the first port of a resonant structure (Fig. 1, elements 38 and 42) wherein an RF source signal (Rf) is electrically coupled to the second port of the first impedance.

Bonn et al. does not disclose a second impedance having a first and second port wherein the first port of the second impedance is electrically coupled to the second port of a resonant

Art Unit: 2634

structure, and wherein an RF source signal (Rfin) is electrically coupled to the the second port of the second impedance. However, it would have been obvious to one of ordinary skill in the art at the time the invention was made to include a second impedance which could perform the same function as the first impedance to provide a desired impedance for processing of the signal at a certain structure within the device. Thus, claim 99 does not constitute patentability.

Allowable Subject Matter

5. Claims 77, 78, 88, 89, 101, 102, 111, and 112 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Li et al (U. S. Patent No. 5, 678, 226) and Kawakami et al. (U. S. Patent No. 5, 760, 632) disclose down-converters comprising of capacitors, switches, and resonant circuits.


7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Curtis B. Odom whose telephone number is 703-305-4097. The examiner can normally be reached on Monday- Friday, 8-5.

Art Unit: 2634

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Stephen Chin can be reached on 703-305-4714. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Curtis Odom
May 28, 2004



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